

| Ref, # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-----------|------|--|----------------------------|---------------------|---------|------------------|
| L1 | 5593 | stor\$3 near2 mask\$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:04 |
| L2 | 916 | register\$3 near3 1 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:04 |
| L3 | 61 | instruction\$1 with 2 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:04 |
| L4 | 11 | instruction\$1 with (address\$2 adj2 mask\$3 adj register\$1) | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:05 |
| L5 | 6104 | address\$2 adj2 mask\$2 mask\$3 adj register\$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:06 |
| L6 | 3539 | read adj modify adj write | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:06 |
| L7 | 364 | (privilege\$1 or kernel) adj3 register\$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:07 |
| L8 | 32 | 6 and 7 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:07 |
| L9 | 0 | 6 with 7 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:07 |
| L10 | 144 | mask\$3 with 6 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:07 |
| L11 | 74 | (supervisor\$1) adj register\$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:08 |
| L12 | 235 | (supervisor\$1) adj3 register\$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:08 |
| L13 | 0 | 6 with 12 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:08 |
| L14 | 0 | psw with 6 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:08 |
| L15 | 0 | psw with 7 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:09 |

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| L16 | 19 | 7 with exception\$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:09 |
| L17 | 279 | 712/229.ccls. | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:09 |
| L18 | 121401 | atomic\$5 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:10 |
| L19 | 1170 | instruction\$1 with 18 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:10 |
| L20 | 703 | interrupt\$3 and (instruction\$1 with atomic\$5) | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:10 |
| L21 | 28547 | (control\$4 or privilege\$1 or status) adj register\$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:11 |
| L22 | 313 | 20 and 21 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:11 |
| L23 | 3539 | read adj modify adj write | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:12 |
| L24 | 51 | 21 with 23 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:13 |
| L25 | 5 | opcode with 23 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:13 |
| L26 | 130 | field\$1 with 23 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:13 |
| L27 | 127 | address\$3 and 26 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:14 |
| L28 | 12 | address\$3 with 26 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:14 |
| L29 | 3 | instruction\$1 with (clear\$3 adj2 interrupt adj mask\$1) | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:15 |
| L30 | 98 | (clear\$3 adj2 interrupt adj mask\$1) | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:16 |
| L31 | 43 | destination with 23 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:16 |

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| L32 | 37 | mask\$3 near3 (general adj purpose adj register\$1) | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:16 |
| L33 | 1196 | kernel adj mode | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:17 |
| L34 | 1 | (mask\$3 near3 (general adj purpose adj register\$1)) and (kernel adj mode) | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:18 |
| L35 | 857 | (clear\$3 or reset\$5) near2 interrupt near3 bit\$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:19 |
| L36 | 449 | (clear\$3 or reset\$5) near2 interrupt adj2 bit\$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:19 |
| L37 | 1 | (privilege\$1 or supervisor\$1 or kernel) near3 ((clear\$3 or reset\$5) near2 interrupt adj2 bit\$1) | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:20 |
| L38 | 1 | (privilege\$1 or supervisor\$1 or kernel) with ((clear\$3 or reset\$5) near2 interrupt adj2 bit\$1) | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:21 |
| L39 | 2180 | (privilege\$1 or supervisor\$1 or kernel) adj mode | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:21 |
| L41 | 134 | 35 and 39 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:23 |
| L42 | 83 | 36 and 39 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:23 |
| L44 | 0 | kernal adj mode adj mask\$3 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:24 |
| L45 | 1 | kernel adj mode adj mask\$3 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:23 |
| L46 | 41 | "5210874".uref. | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:24 |
| L47 | 3661 | (status adj register) near3 (updat\$3 or clear\$1 or set\$4) | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:25 |
| L48 | 50 | mask\$3 with 47 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:25 |
| L49 | 7 | kernel adj mode adj instruction\$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:25 |

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| L50 | 56 | (privilege or privelege) adj instruction\$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:26 |
| L51 | 603 | (privilege\$1 or privelege\$1) adj instruction\$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:26 |
| L52 | 1930 | register adj field\$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:27 |
| L53 | 87 | 51 and 52 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:27 |
| L54 | 141 | 712/224.ccls. | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:27 |
| L55 | 17 | (kernel or privilege\$1) and 54 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:32 |
| L56 | 279 | 712/229.ccls. | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:32 |
| L57 | 490 | 712/244.ccls. | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:32 |
| L59 | 892 | 54 or 56 or 57 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:32 |
| L60 | 199 | (kernel or privilege\$1) and 59 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:33 |
| L61 | 105 | 60 and mask\$3 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:34 |
| L62 | 179925 | ((single or one) adj cycle) or atomic\$5 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:34 |
| L63 | 48 | 61 and 62 | US-PGPUB; USPAT; EPO | OR | OFF | 2005/07/06 18:34 |